

PTO/SB/08b (08-03)

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**INFORMATION DISCLOSURE
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Application Number	10/617117
Filing Date	July 10, 2003
First Named Inventor	Michael Robert Samuels Et. Al.
Art Unit	1756
Examiner Name	Unknown
Attorney Docket Number	AD6877USNA

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
h		Patent Abstracts of Japan, Publ. No. 04055437, 2-24-1992, Matsushita Electric Works, Ltd. "PREPREG FOR LAMINATED BOARD WITH LOW DIELECTRIC CONSTANT"	<input type="checkbox"/>
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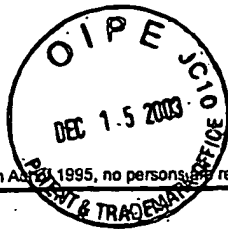
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Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
<i>next</i> <i>RM</i>		US- 4,118,372	10-03-1978	Schaeffgen	
		US- 5,348,990	09-20-1994	Walpita	
		US-			
		US-			
		US-			
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FOREIGN PATENT DOCUMENTS

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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
<i>next</i> <i>not present</i>		EP 0 512 401 A2	11-11-1992	Internl. Business Machine		
		EP 0 870 795 A1	10-14-1998	Sumitomo Chem. Co.		
		WO 94/26802	11-24-1994	E. I. Dupont		
		WO 99/52978	10-21-1999	E. I. Dupont		

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